



## 2<sup>nd</sup> International Conference



# “2019-Women Institute of Technology Conference on Electrical and Computer Engineering”

## (WITCON ECE)

### Schedule Day-2

Date: 23 Nov, 2019

Venue: Seminar Hall, Women Institute of Technology, (UTU Campus), Dehradun

#### Guest:

1. Dr. Durgesh Pant, Director, Uttarakhand Science Education and Research Centre (USERC) & Professor, Uttarakhand Open University
2. Dr. Lalita Goyal, Principal Scientist, CSIR, New Delhi
3. Dr. Geetam S. Tomar, Gwalior, Madhya Pradesh
4. Mrs. Archana Jain, Industrialist, Akums Drugs & Pharmaceuticals Ltd, Haridwar

|                 |  |
|-----------------|--|
| 9am-10:15am     | Registration and Tea   |
| 10:15am-10:30am | Welcome Address followed by Glimpses of Day 1  |
| 10:30am-12:30pm | Welcome address and briefing about the Conference<br>(1) Address by <b>Dr. Durgesh Pant</b><br>(2) Address by <b>Dr. Lalita Goyal</b><br>(3) Address by <b>Dr. G. S. Tomar</b> |
| 10:30pm-01:00pm | Three Technical Parallel Session   |
| 1:00pm-2:00pm   | Lunch  |
| 2:00pm-3:00pm   | Valedictory Session  |
|                 | Concluding Address   |
|                 | Dispersal followed by Group Photograph   |

### Technical Session – (Parallel Session-I-Oral Presentation)

# Electrical Engineering

Time:10:30 AM – 1:00PM

23 Nov, 2019

Venue : Room No - 111

Session Chair: Dr. Prabhakar Tiwari

Student Volunteers: (1)

(2)

| S.No. | Paper ID | Paper Title  | Author Name   |
|-------|----------|--|---|
| 1.    | 83       | Blind Equalization with NARX Neural Networks   | Vipra Singh and Sanjay Mathur   |
| 2.    | 90       | Voltage Oriented Control of Grid-tied Solar PV System                                    | KanchanMatiyali, Sandeep Kumar Goel and Hitesh Joshi                    |
| 3.    | 133      | Extraction of the solar PV module parameters using chicken swarm optimization technique  | Abhishek Sharma, Rupendra Kumar Pachauri, Abhinav Sharma and Nikhil Raj |
| 4.    | 140      | Simulation and Modeling of a Wind Turbine using PMSG with Maximum Power Tracking Control | Hitesh Joshi, A. K. Swami and Kanchan Matiyali                          |
| 5.    | 145      | Experimental Analysis of Power Quality of Semi Controlled Rectifier System               | Mahashree Tamta, Rituraj and Mayank Chaturvedi                          |

# Technical Session – (Parallel Session-II-Oral Presentation)

## Electronics and Communication Engineering

Time: 10.30 AM – 1:00PM

23 Nov, 2019

Venue : Seminar Hall

Session Chair: Dr .Kamal Kumar Sharma

Student Volunteers: (1)

(2)

| S.No. | Paper ID | Paper Title   | Author Name   |
|-------|----------|---|---|
| 1.    | 37       | Implementation of Vehicle Security System using GPS,GSM and Biometric   | Mridhula Ramesh,<br>Akruthi Srikanth,<br>Nandhini<br>Kalyanasundaram, Meena<br>Sugumar, Joseph Gladwin<br>and Rajavel R |
| 2.    | 46       | New Current Mode Multi-Function Filter and Single-Resistance-Controlled Oscillator Employing VDCC and Only Grounded Passive Components. | Tajinder Arora  |
| 3.    | 64       | Analysis of Nanosheet Field Effect Transistor (NSFET) for device and circuit perspective.   | Priyesh Kumar, Sarita<br>Yadav and Pankaj Kumar<br>Pal  |
| 4.    | 82       | High Performance 20-T based Hybrid Full Adder using 90nm CMOS technology.   | Jyoti Kandpal, Abhishek<br>Tomar, Kailash Pandey<br>and Mayur Aggarwal  |
| 5.    | 84       | A Sense-Amplifier Based Flip-Flop with Symmetric Latch Design   | Kailash Pandey,<br>Abhishek Tomar and<br>Jyoti Kandpal  |
| 6.    | 113      | DESIGN AND VHDL IMPLEMENTATION OF ZIGBEE STAR AND MESH TOPOLOGY USING FPGA IMPLEMENTATION   | Amit Kumar, Krishan<br>Chandra Mishra and Ajay<br>Rawat   |
| 7.    | 121      | Design of Optical Integrated Circuit dedicated to XOR/XNOR Gates  | Vivek Kumar Srivastava,<br>Shashank Chauhan,<br>Simran Aneja, Amrindra<br>Pal and Sandeep Sharma.                       |
| 8.    | 123      | Design, Analysis and Comparison of PFD Architectures for Fast Locking Frequency Synthesizer   | Avinash Sharma, Gaurav<br>Kumar Sharma, Arun<br>Kishor Johar, Tangudu<br>Bharat Kumar and<br>Dharmendar<br>Boolchandani |

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|-----|-----|--|---|
| 9.  | 127 | Performance Optimization of Vertical Gaussian Doped SOI Junctionless FET with Substrate Bias | Dr Balraj Singh, Aanchal Garg and Dr Yashvir Singh            |
| 10. | 137 | SIW Based Self-Diplexing Dumbbell Slot Antenna for X- Band Application                       | Padmini Nigam, Arjuna Muduli, Sandeep Sharma and Amrindra Pal |
| 11. | 143 | The Substrate Integrated Waveguide Based Self-Triplexing Cross Slot Antenna                  | Padmini Nigam, Arjuna Muduli, Sandeep Sharma and Amrindra Pal |

## Technical Session – (Parallel Session-III-Oral Presentation)

### Computer Science and Engineering

**Time: 10.30 AM – 1:00PM**

**23 Nov, 2019**

**Venue : Room No- 104**

**Session Chair: Dr. Pramod Kumar & Dr.K.S.Wasla (Invitee talk)**

**Student Volunteers: (1)**

**(2)**

| S.No. | Paper ID | Paper Title  | Author Name   |
|-------|----------|--|---|
| 1.    | 22       | Performance Improvement in Handwritten Devanagari Character Classification                     | Shivansh Gupta and Ramesh Kumar Mohapatra   |
| 2.    | 65       | Smart Meters for Domestic Consumers: Innovative Methods for Identifying Appliances using NIALM | Sunil Semwal, Manoj Badoni and Nishant Saxena   |
| 3.    | 94       | Profit Maximization Bidding Strategy for a GENCO using Whale Optimization Algorithm            | Pooja Jain and Akash Saxena   |
| 4.    | 128      | Identifying and using Viable Alternative Energy Resources (WIND Energy)                        | Neeraj Sagar, Akshay Tiwari, Sachin Kumar, Lokesh Kumar, Monika Belwal and Sandeep Kumar                        |
| 5.    | 129      | Location-aware IoT Search Framework based on Data Messaging and Aggregation Techniques         | Santosh Pattar, Lakshmi K N, Darshil Vala, Venkatesh, Rajkumar Buyya, Venugopal K R, S SIyengar and L M Patnaik |
| 6.    | 144      | Emerging Potential Areas for Adoption of Blockchain Technology in India                        | Sumaiya and Ajay Kumar Bharti   |



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| 7.                | 160 | Material Optimization for Soft Actuator Based on Vibration | Brijesh Prasad, Narendra Gariya, Pushpendra Kumar, Varij Panwar and Pravin Patil |
| 01:00pm – 02:00pm |     | <b>Lunch</b>   |  |

